





# VG4000<sup>+</sup> Block Diagram

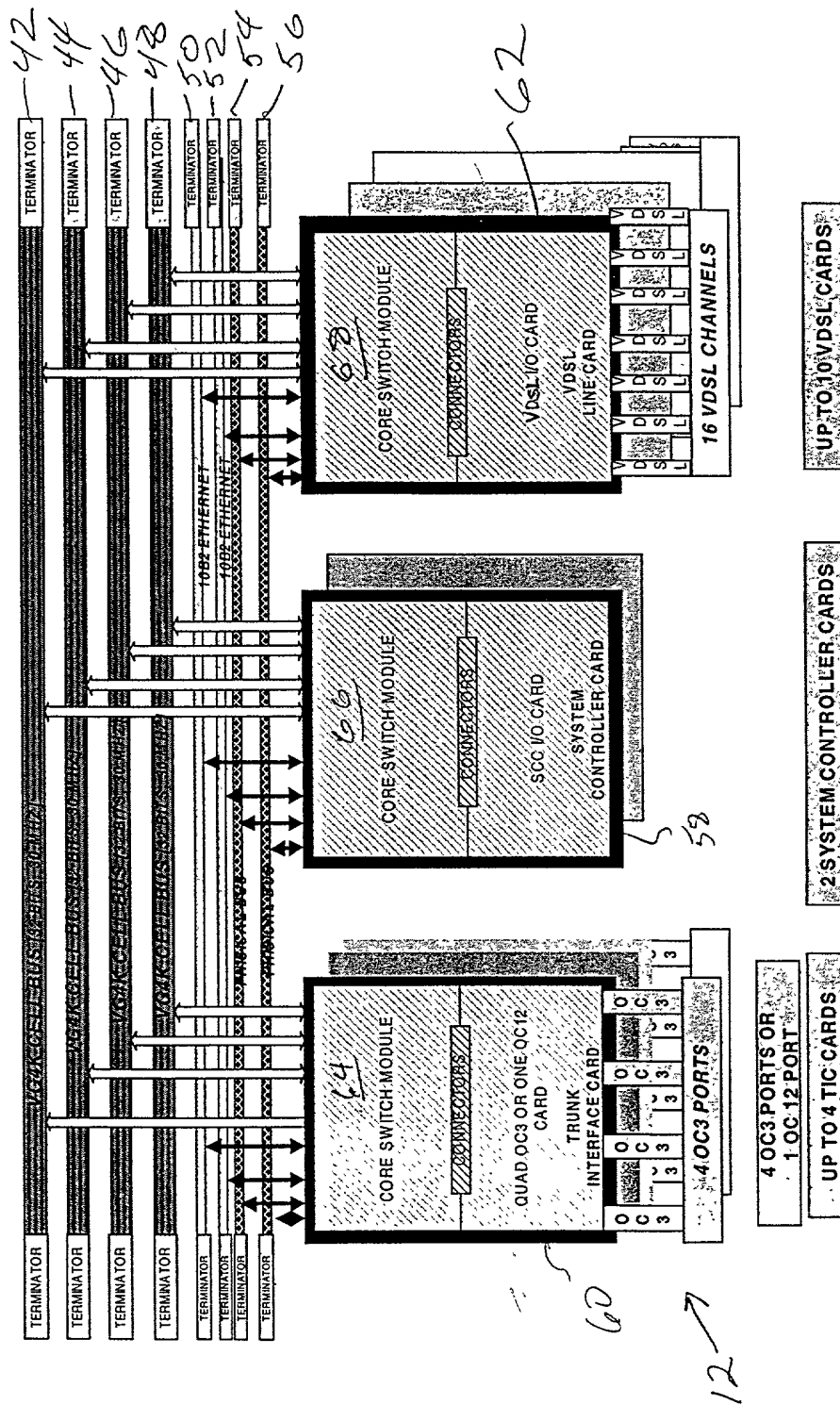


FIG. 2

VG 4000<sup>+</sup> ARCHITECTURE





# System Controller I/O Card

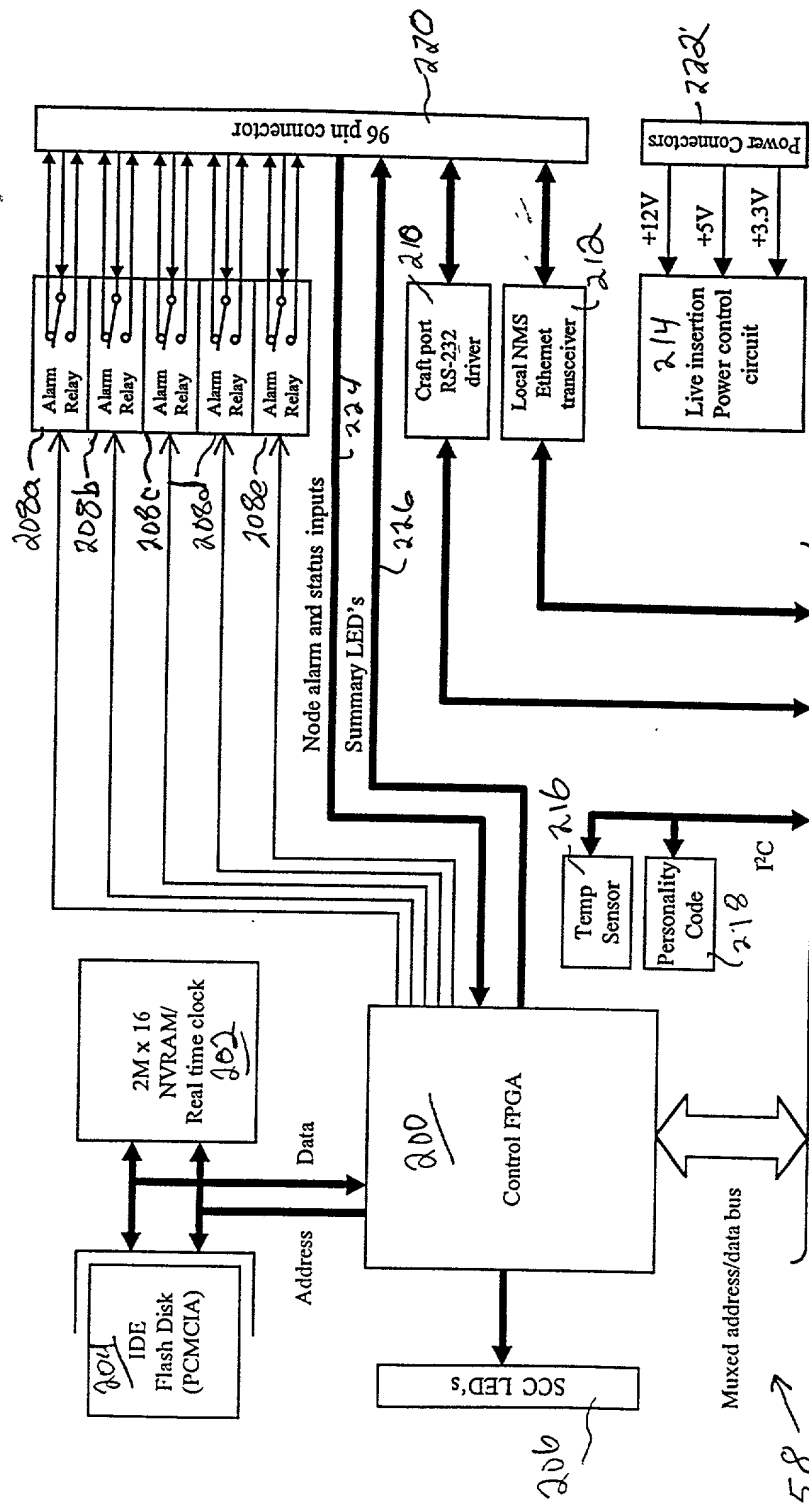
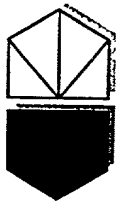


FIG. 1



# Quad OC3 Block Diagram

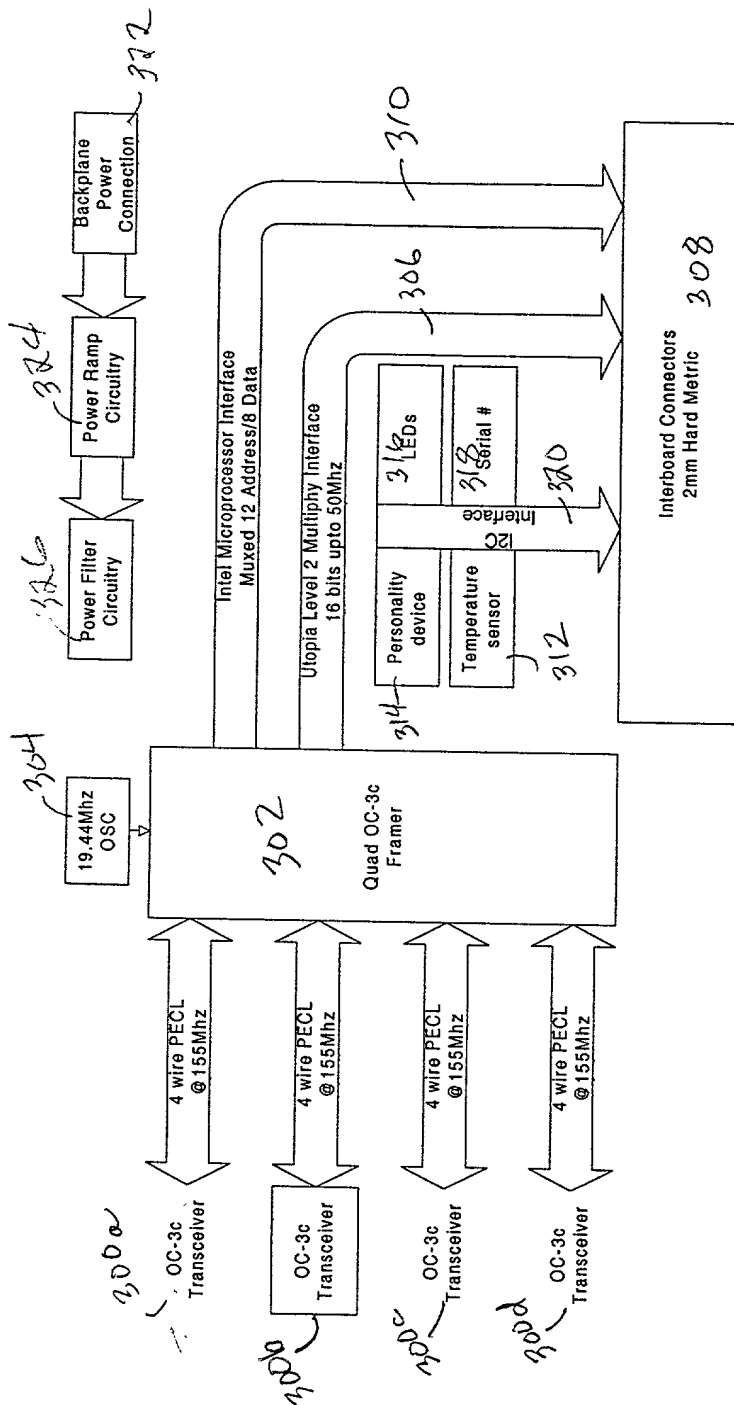
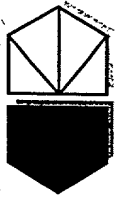
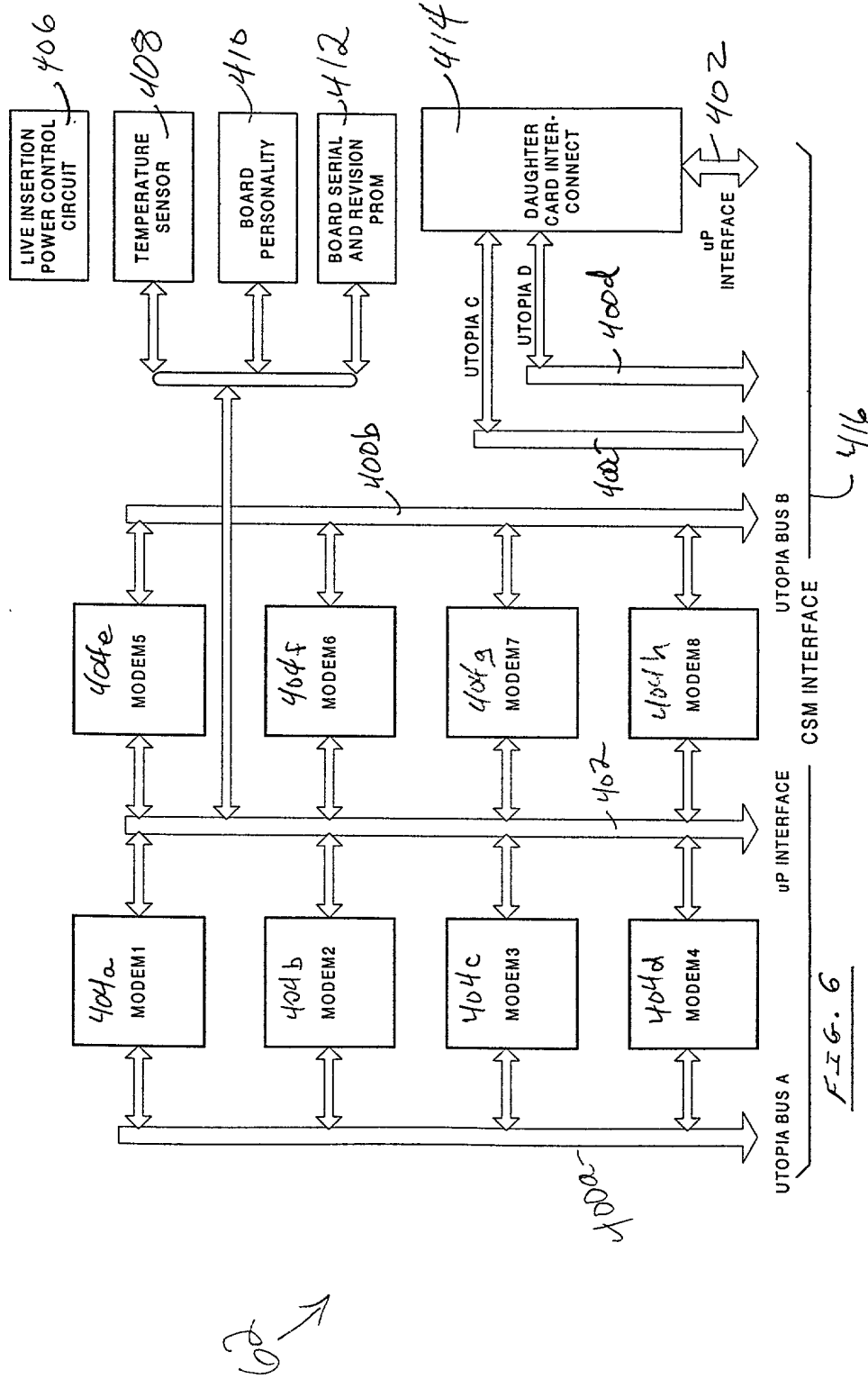
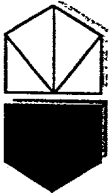


FIG. 5



# VLC - VDSL Line Card





# ViaJet A40x Block Diagram

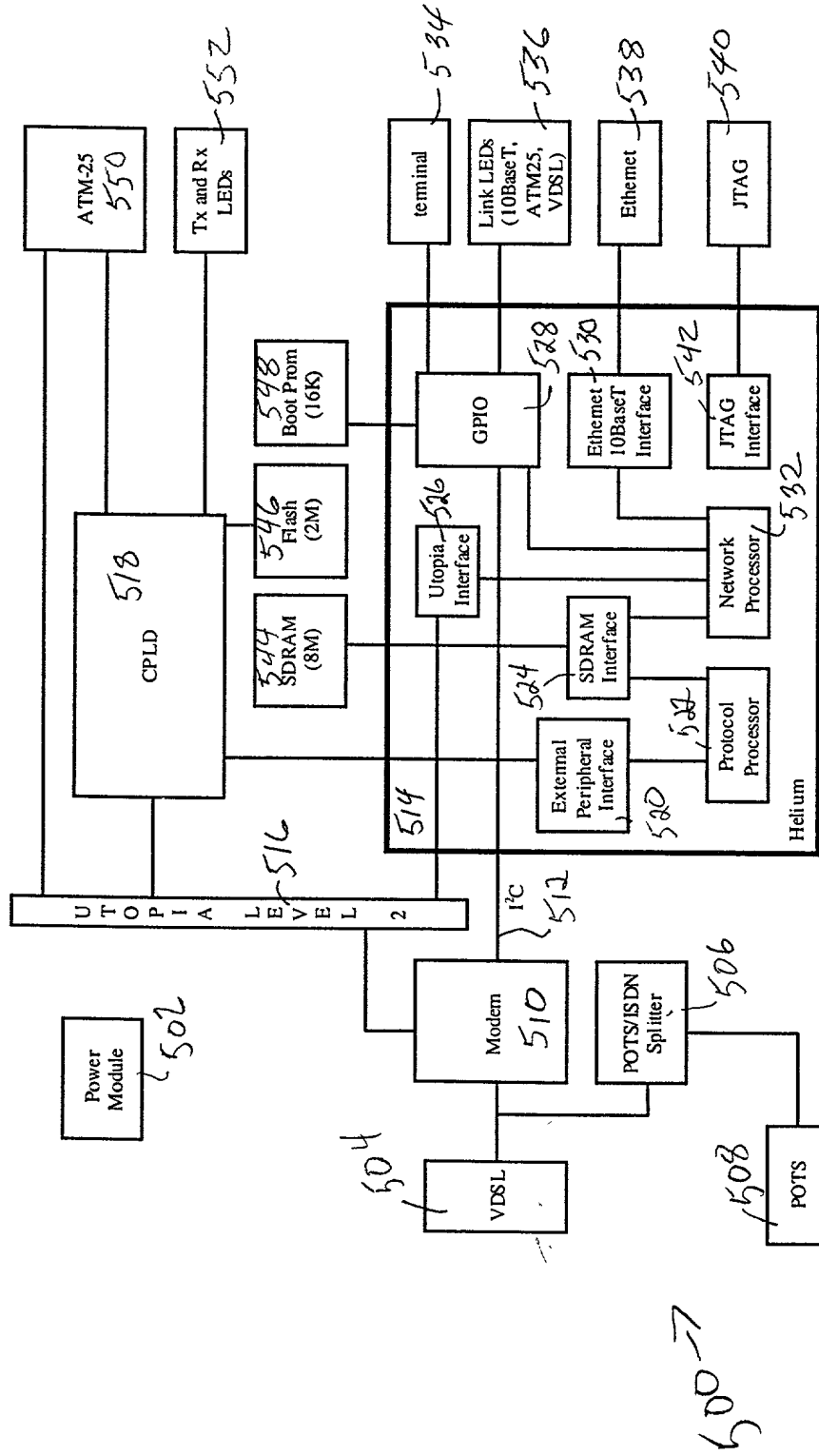


FIG. 7a



# ViaWay A406

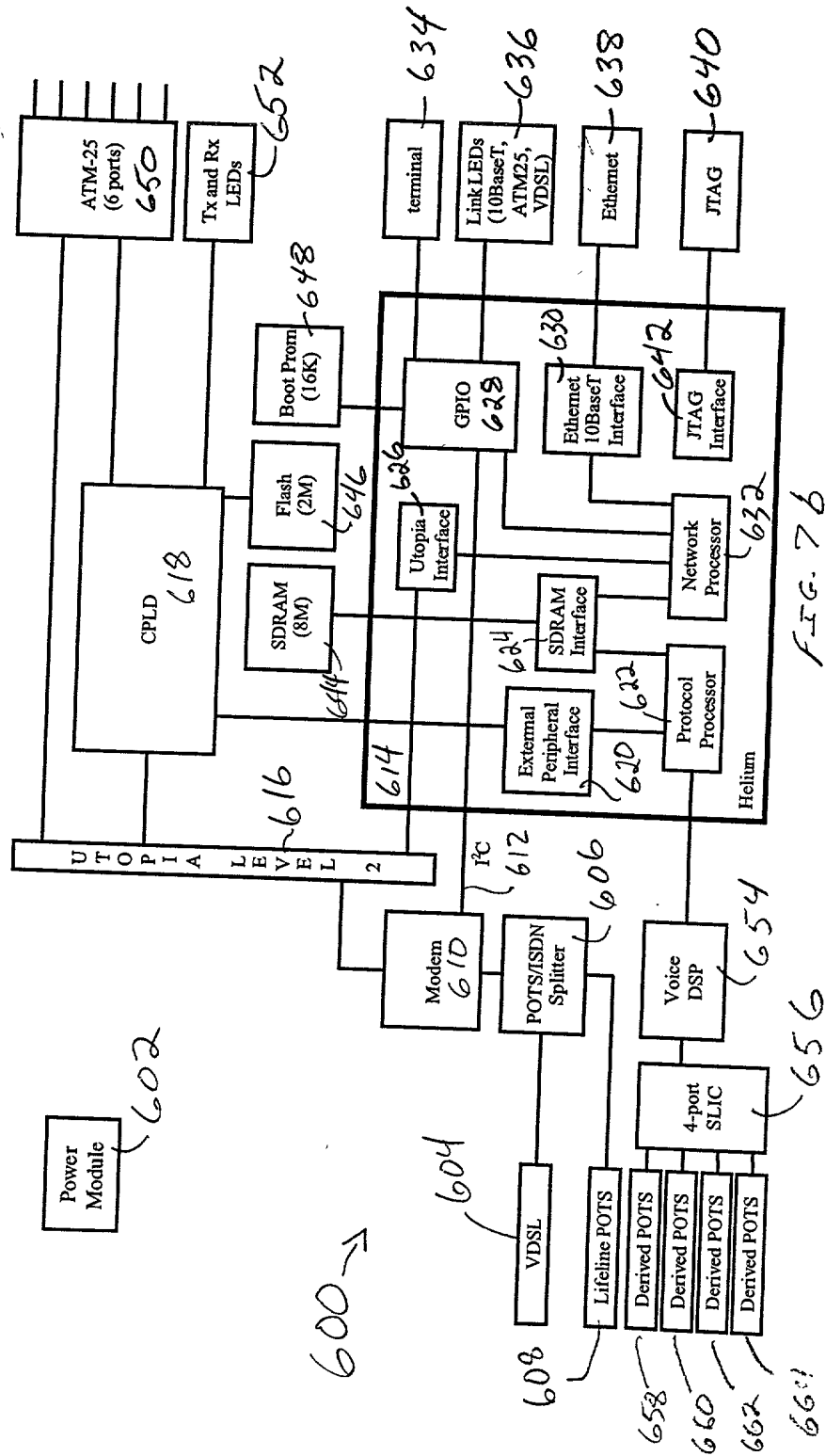
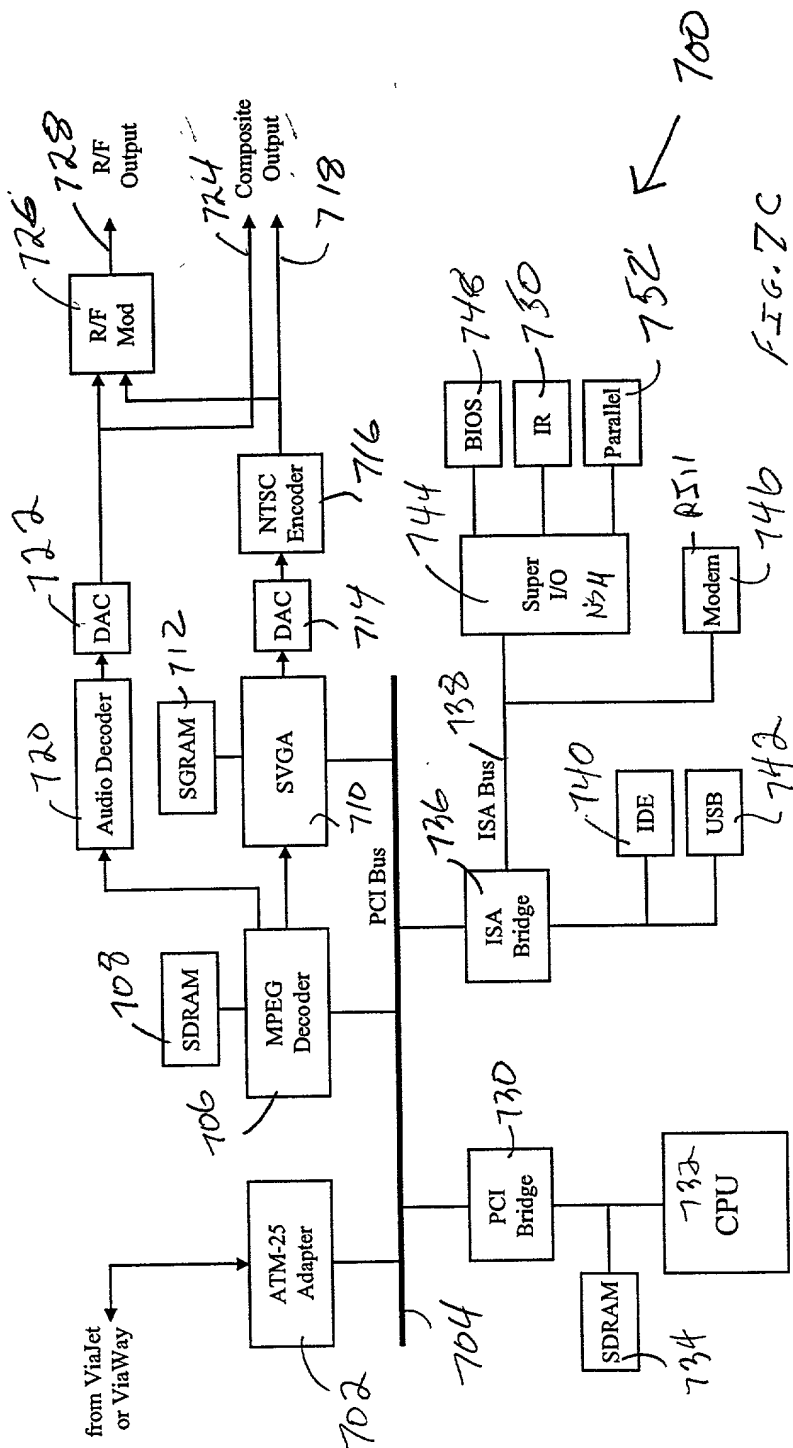


FIG. 76





# Typical Digital Set-Top Box



802

FIG. 8

804

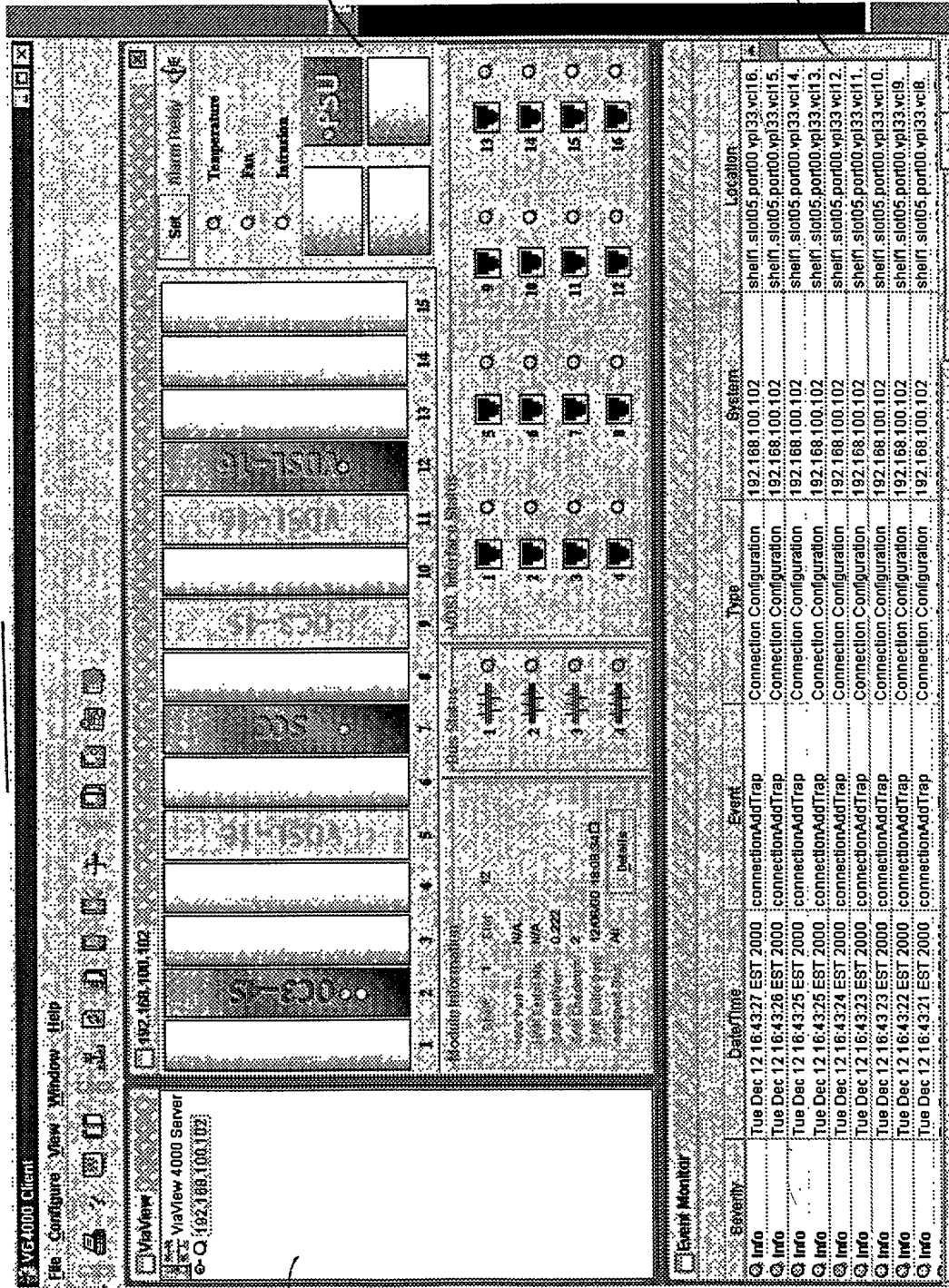
806

808

800

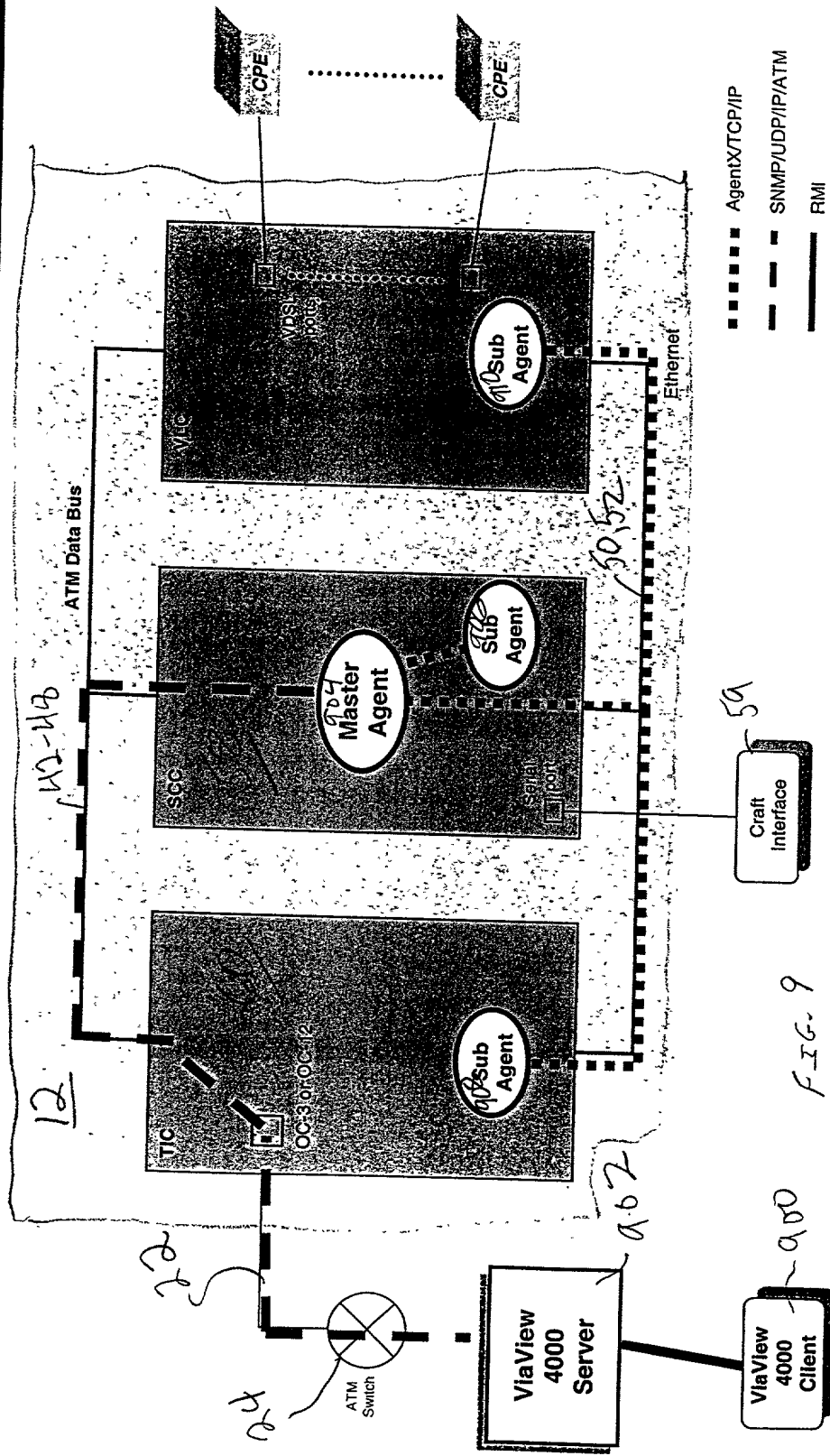
810

812



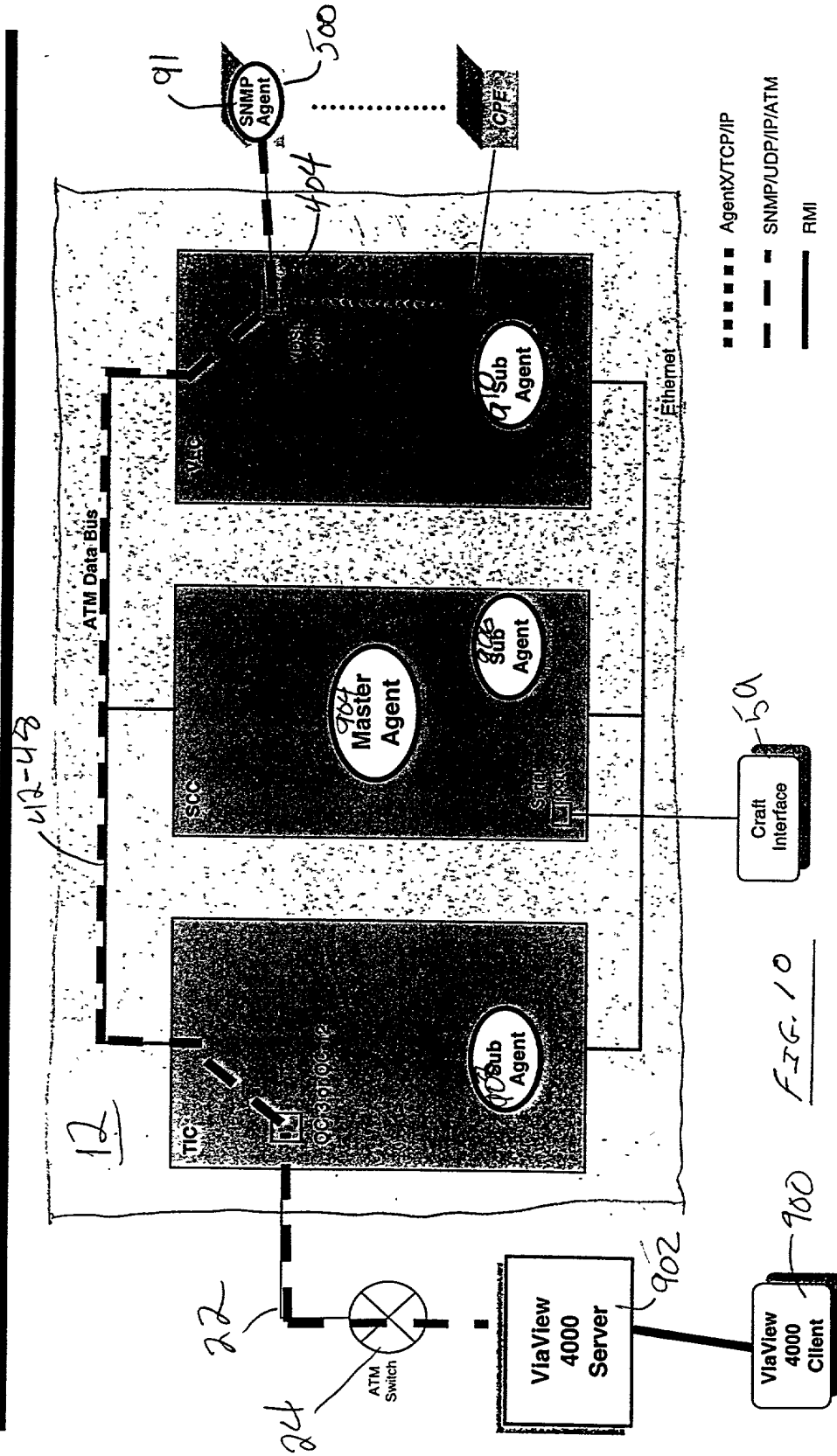


# Node Management Paths

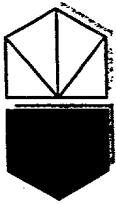




# CPE Management Path







# CATV/Broadcast Management

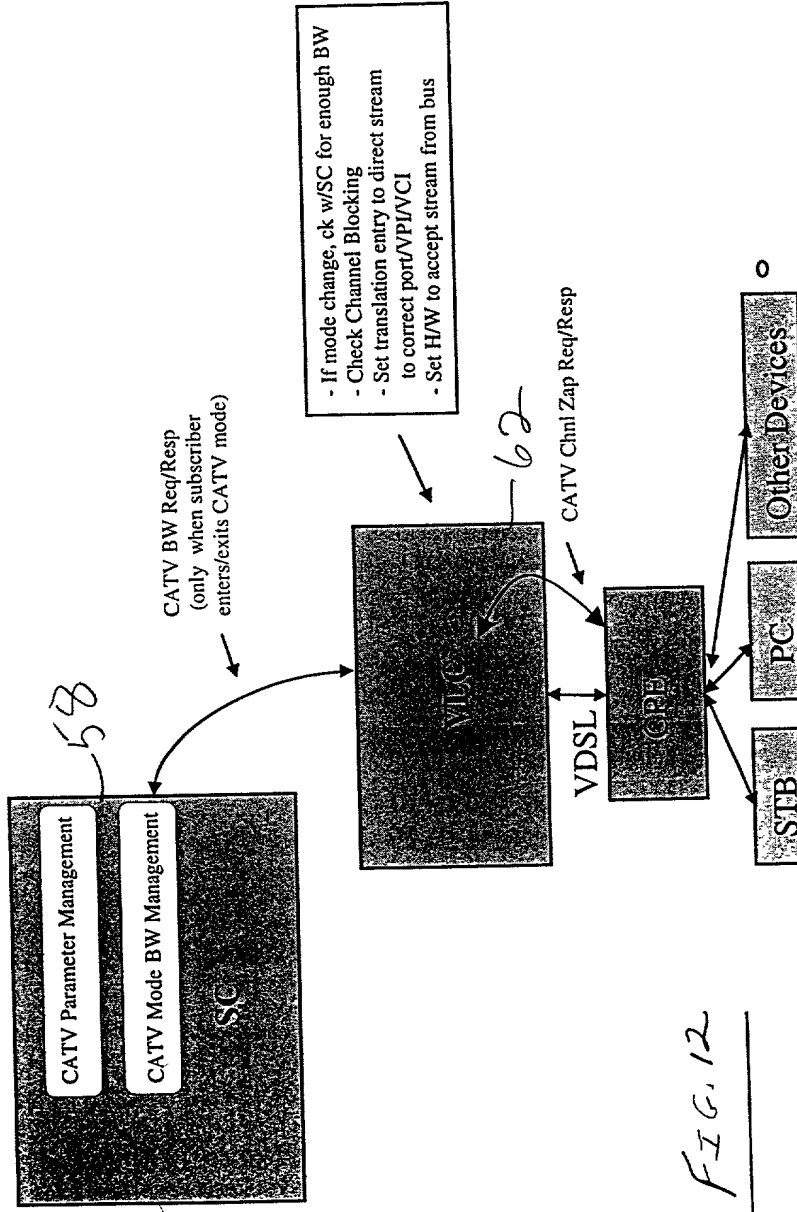


FIG. 12